10065660 .092606

TRANSMITTAL FORM



Electronic Version 1.0.3

Stylesheet Version: 1.0

Submission Type: Utility

Patent Filing

Attorney Docket Number:

2000P19153US

INTEGRATED CIRCUITS WITH PARALLEL SELF-TESTING

First Named Inventor: Mr. Raj Kumar Jain

SUBMITTED BY

Name: Mr. Dexter Kam Chin

Registration Number: 38842

Electronic Signature Mark: DKC Date Signed: 20020926

I certify that the use of this system is for OFFICIAL correspondence between patent applicants or their representatives and the USPTO. Fraudulent or other use besides the filing of official correspondence by authorized parties is strictly prohibited, and subject to a fine and/or imprisonment under applicable law.

I, the undersigned, certify that I have viewed a display of document(s) being electronically submitted to the United States Patent and Trademark Office, using either the USPTO provided style sheet or software, and that this is the document(s) I intend for initiation or further prosecution of a patent application noted in the submission. This document(s) will become part of the official electronic record at the USPTO.

Attached Files:

declaration ODP1.tif

declaration ODP2.tif

us-information-disclosure-statement 00P19153USids.xml

patent-assignments 00P19153USasgn.xml

fee-transmittal

bibd-transmittal

specification

00P19153USfee.xml 00P19153USapds.xml 00P19153US.xml

Attached Image File(s):

ODP1.tif

ODP2.tif

Comments:

FEE TRANSMITTAL

Electronic Version 1.1.0 Stylesheet Version: 1.0

Patent fees are subject to annual revisions on or about October 1st of each year.

Large Entity

TOTAL FEES AUTHORIZED: \$852

The commissioner is hereby authorized to charge indicated processing and/or publication fees and credit any overpayments to:

Deposit Account

502388

Number:

SIEMENS INTELL PROPERTY TECHNOLOGY

Deposit Account Name: CONSULTING P/L

Charge Any Additional Fee Required Under 37 C.F.R. Sections 1.16 and 1.17.

Charge Assignment Fees Required Under 37 C.F.R. Section 1.21 (h).

SUBMITTED BY

Authorized Name:

Dexter Chin

Electronic Signature Mark:

DKC

Date Signed:

20020926

BASIC FILING FEE

Fee Description	Fee Code	Fee Paid
Utility Filing Fee	101	\$ 740

Subtotal For Basic Filing Fee: \$ 740

EXTRA CLAIM FEES

	Fee Code	Fee	Extra Claims	Fee Paid
Total Claims: 24	103	\$ 18	4	\$ 72

1005520.092602

Independent Claims: 1 102 \$ 84	0 \$ 0	
---------------------------------	--------	--

Subtotal For Extra Claims Fees: \$ 72

ADDITIONAL FEES

Fee Description	Number	Quantity	Fee Code	Amount	Fee Paid
Recording Each Patent Assignment Per Property Fee	00000000	1	581	\$ 40	\$ 40

Subtotal For Additional Fees: \$ 40

Electronic Filing System (EFS) Data Electronic Patent Application Submission USPTO Use Only

EFS ID:

18313

Application ID:

10065220

Title of Invention:

INTEGRATED CIRCUITS WITH

PARALLEL SELF-TESTING

First Named Inventor:

Raj Kumar Jain

Domestic/Foreign Application:

Domestic Application

Filing Date:

null

Effective Receipt Date:

2002-09-26

Submission Type:

Utility Patent Filing

Filing Type:

new-utility

Confirmation Number:

0

Attorney Docket Number:

2000P19153US

cn=Dexter Kam Chin, ou=Registered Attorneys, ou=Patent and Trademark Office, ou=Department of Commerce, o=U.S.

Digital Certificate Holder:

-

Government, c=US

Certificate Message Digest:

B1hz0Q0faDQF0w816QVvcw==

Total Fees Authorized:

\$852.0

Payment Category:

DA - Deposit Account

Deposit Account Number:

502388

Deposit Account Name:

Dexter Chin